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Radio Frequency Integrated Circuit Design
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EMMALEE LEWIS

The Design and Implementation of Low-Power CMOS Radio Receivers Pearson

This book, first published in 2004, is an expanded and revised edition of Tom Lee's acclaimed RFIC text.

Radio Frequency Integrated Circuit Design John Wiley & Sons

Highlighting the challenges RF and microwave circuit designers face in their day-to-day tasks, RF and Microwave Circuits, Measurements, and Modeling explores RF and microwave circuit designs in terms of performance and critical design specifications. The book discusses transmitters and receivers first in terms of functional circuit block and then examines each block individually. Separate articles consider fundamental amplifier issues, low noise amplifiers, power amplifiers for handset applications and high power, power amplifiers. Additional chapters cover other circuit functions including oscillators, mixers, modulators, phase locked loops, filters and multiplexers. New chapters discuss high-power PAs, bit error rate testing, and nonlinear modeling of heterojunction bipolar transistors, while other chapters feature new and updated material that reflects recent progress in such areas as high-volume testing, transmitters and receivers, and CAD tools. The unique behavior and requirements associated with RF and microwave systems establishes a need for unique and complex models and simulation tools. The required toolset for a microwave circuit designer includes unique device models, both 2D and 3D electromagnetic simulators, as well as frequency domain based small signal and large signal circuit and system simulators. This unique suite of tools requires a design procedure that is also distinctive. This book examines not only the distinct design tools of the microwave circuit designer, but also the design procedures that must be followed to use them effectively.

Millimeter-Wave Circuits for 5G and Radar CRC Press

Featuring an extensive 40 page tutorial introduction, this carefully compiled anthology of 65 of the most important papers on phase-locked loops and clock recovery circuits brings you comprehensive coverage of the field—all in one self-contained volume. You'll gain an understanding of the analysis, design, simulation, and implementation of phase-locked loops and clock recovery circuits in CMOS and bipolar technologies along with valuable insights into the issues and trade-offs associated with phase locked systems for high speed, low power, and low noise.

Design of Analog CMOS Integrated Circuits NC State University

Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The book's unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

Analog Integrated Circuits for Communication Oxford University Press, USA

SiGe HBTs is a hot topic within the microelectronics community because of its applications potential within integrated circuits operating at radio frequencies. Applications range from high speed optical networking to wireless communication devices. The addition of germanium to silicon technologies to form silicon germanium (SiGe) devices has created a revolution in the semiconductor industry. These transistors form the enabling devices in a wide range of products for wireless and wired communications. This book features: SiGe products include chip sets for wireless cellular handsets as well as WLAN and high-speed wired network applications Describes the physics and technology of SiGe HBTs, with coverage of Si and Ge bipolar transistors Written with the practising engineer in mind, this book explains the operating principles and applications of bipolar transistor technology. Essential reading for practising microelectronics engineers and researchers. Also, optical communications engineers and communication technology engineers. An ideal reference tool for masters level students in microelectronics and electronics engineering.

Design of Integrated Circuits for Optical Communications Newnes

Microwave and RF Design: Radio Systems is a circuits- and systems-oriented approach to modern microwave and RF systems. Sufficient details at the circuits and sub-system levels are provided to understand how modern radios are implemented. Design is emphasized throughout. The evolution of radio from what is now known as 0G, for early radio, through to 6G, for sixth generation cellular radio, is used to present modern microwave and RF engineering concepts. Two key themes unify the text: 1) how system-level decisions affect component, circuit and subsystem design; and 2) how the capabilities of technologies, components, and subsystems impact system design. This book is suitable as both an undergraduate and graduate textbook, as well as a career-long reference book.

Key Features * The first volume of a comprehensive series on microwave and RF design * Open access ebook editions are hosted by NC State University Libraries at <https://repository.lib.ncsu.edu/handle/1840.20/36776> * 31 worked examples * An average of 38 exercises per chapter * Answers to selected exercises * Coverage of cellular radio from 1G through 6G * Case study of a software defined radio illustrating how modern radios partition functionality between analog and digital domains * A companion book, Fundamentals of Microwave and RF Design, is suitable as a comprehensive undergraduate textbook on microwave engineering

High-Speed CMOS Circuits for Optical Receivers McGraw-Hill College

It is hardly a profound observation to note that we remain in the midst of a wireless revolution. In 1998 alone, over 150 million cell phones were sold worldwide, representing an astonishing 50% increase over the previous year. Maintaining such a remarkable growth rate requires constant innovation to decrease cost while increasing performance and functionality. Traditionally, wireless products have depended on a mixture of semiconductor technologies, spanning GaAs, bipolar and BiCMOS, just to name a few. A question that has been hotly debated is whether CMOS could ever be suitable for RF applications. However, given the acknowledged inferiority of CMOS transistors relative to those in other candidate technologies, it has been argued by many that "CMOS RF" is an oxymoron, an endeavor best left cloistered in the ivory towers of academia. In rebuttal, there are several compelling reasons to consider CMOS for wireless applications. Aside from the exponential

device and density improvements delivered regularly by Moore's law, only CMOS offers a technology path for integrating RF and digital elements, potentially leading to exceptionally compact and low-cost devices. To enable this achievement, several thorny issues need to be resolved. Among these are the problem of poor passive components, broadband noise in MOSFETs, and phase noise in oscillators made with CMOS. Beyond the component level, there is also the important question of whether there are different architectural choices that one would make if CMOS were used, given the different constraints.

Microwave Transistor Amplifiers RF Microelectronics

Essential reading for experts in the field of RF circuit design and engineers needing a good reference. This book provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters. It also covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail. Provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters Covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail

Microwave and RF Design, Volume 1 Tata McGraw-Hill Education

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

Theory and Design Cambridge University Press

The recent shift in focus from defense and government work to commercial wireless efforts has caused the job of the typical microwave engineer to change dramatically. The modern microwave and RF engineer is expected to know customer expectations, market trends, manufacturing technologies, and factory models to a degree that is unprecedented in the

List of Available Publications CRC Press

This extensively revised edition offers a comprehensive, practical, up-to-date understanding of how to tackle a power amplifier design with confidence and quickly determine the cause of malfunctioning hardware.

Radio Frequency Integrated Circuits and Systems Prentice Hall PTR

Analog Integrated Circuits for Communication: Principles, Simulation and Design, Second Edition covers the analysis and design of nonlinear analog integrated circuits that form the basis of present-day communication systems. Both bipolar and MOS transistor circuits are analyzed and several numerical examples are used to illustrate the analysis and design techniques developed in this book. Especially unique to this work is the tight coupling between the first-order circuit analysis and circuit simulation results. Extensive use has been made of the public domain circuit simulator Spice, to verify the results of first-order analyses, and for detailed simulations with complex device models. Highlights of the new edition include: A new introductory chapter that provides a brief review of

communication systems, transistor models, and distortion generation and simulation. Addition of new material on MOSFET mixers, compression and intercept points, matching networks. Revisions of text and explanations where necessary to reflect the new organization of the book Spice input files for all the circuit examples that are available to the reader from a website. Problem sets at the end of each chapter to reinforce and apply the subject matter. An instructors solutions manual is available on the book's webpage at springer.com. Analog Integrated Circuits for Communication: Principles, Simulation and Design, Second Edition is for readers who have completed an introductory course in analog circuits and are familiar with basic analysis techniques as well as with the operating principles of semiconductor devices. This book also serves as a useful reference for practicing engineers.

Advances in Electronics, Communication and Computing Artech House Publishers

This second edition of the highly acclaimed RF Power Amplifiers has been thoroughly revised and expanded to reflect the latest challenges associated with power transmitters used in communications systems. With more rigorous treatment of many concepts, the new edition includes a unique combination of class-tested analysis and industry-proven design techniques. Radio frequency (RF) power amplifiers are the fundamental building blocks used in a vast variety of wireless communication circuits, radio and TV broadcasting transmitters, radars, wireless energy transfer, and industrial processes. Through a combination of theory and practice, RF Power Amplifiers, Second Edition provides a solid understanding of the key concepts, the principle of operation, synthesis, analysis, and design of RF power amplifiers. This extensive update boasts: up to date end of chapter summaries; review questions and problems; an expansion on key concepts; new examples related to real-world applications illustrating key concepts and brand new chapters covering 'hot topics' such as RF LC oscillators and dynamic power supplies. Carefully edited for superior readability, this work remains an essential reference for research & development staff and design engineers. Senior level undergraduate and graduate electrical engineering students will also find it an invaluable resource with its practical examples & summaries, review questions and end of chapter problems. Key features: • A fully revised solutions manual is now hosted on a companion website alongside new simulations. • Extended treatment of a broad range of topologies of RF power amplifiers. • In-depth treatment of state-of-the art of modern transmitters and a new chapter on oscillators. • Includes problem-solving methodology, step-by-step derivations and closed-form design equations with illustrations.

From Circuit Level to Architecture Level Wiley

RF Microelectronics Prentice Hall PTR

Microelectronics Springer Science & Business Media

Understand the RF and Digital Signal Processing Principles Driving Software-defined Radios! Software-defined radio (SDR) technology is a configurable, low cost, and power efficient solution for multimode and multistandard wireless designs. This book describes software-defined radio concepts and design principles from the perspective of RF and digital signal processing as performed within this system. After an introductory overview of essential SDR concepts, this book examines signal modulation techniques, RF and digital system analysis and requirements, Nyquist and oversampled data conversion techniques, and multirate digital signal processing.. KEY TOPICS • Modulation

techniques Master analog and digital modulation schemes •RF system-design parameters Examine noise and link budget analysis and Non-linear signal analysis and design methodology •Essentials of baseband and bandpass sampling and gain control IF sampling architecture compared to traditional quadrature sampling, Nyquist zones, automatic gain control, and filtering •Nyquist sampling converter architectures Analysis and design of various Nyquist data converters •Oversampled data converter architectures Analysis and design of continuous-time and discrete-time Delta-Sigma converters •Multirate signal processing Gain knowledge of interpolation, decimation, and fractional data rate conversion *Offers readers a powerful set of analytical and design tools *Details real world designs *Comprehensive coverage makes this a must have in the RF/Wireless industry

Circuit Design for RF Transceivers Springer Science & Business Media

Microelectronic Circuits by Sedra and Smith has served generations of electrical and computer engineering students as the best and most widely-used text for this required course. Respected equally as a textbook and reference, "Sedra/Smith" combines a thorough presentation of fundamentals with an introduction to present-day IC technology. It remains the best text for helping students progress from circuit analysis to circuit design, developing design skills and insights that are essential to successful practice in the field. Significantly revised with the input of two new coauthors, slimmed down, and updated with the latest innovations, Microelectronic Circuits, Eighth Edition, remains the gold standard in providing the most comprehensive, flexible, accurate, and design-oriented treatment of electronic circuits available today.

Integrated Circuit Design for High-speed Frequency Synthesis Springer Science & Business Media

. DC CIRCUITS. 1. Components, Quantities, and Units. 2. Voltage, Current, and Resistance in Electric Circuits. 3. Ohm's Law, Energy, and Power. 4. Series Circuits. 5. Parallel Circuits. 6. Series-Parallel Circuits. 7. Magnetism and Electromagnetism. II. AC CIRCUITS. 8. Introduction to Alternating Current and Voltage. 9. Capacitors. 10. RC Circuits. 11. Inductors. 12. RL Circuits. 13. RLC Circuits and Resonance. 14. Transformers. 15. Pulse Response of Reactive Circuits. III. DEVICES. 16. Introduction to Semiconductors 17. Diodes and Applications. 18. Transistors and Thyristors. 19. Amplifiers and Oscillators. 20. Operational Amplifiers (Op-Amps). 21. Basic Applications of Op-Amps. APPENDICES. A. Table of Standard Resistor Values. B. Batteries. C. Capacitor Color Coding and Labeling. D. The Current Source, Nortons Theorems and Millman's Theorem. E. Devices Data Sheets. Answers to Odd-Numbered Problems. Glossary. Index.

High-Frequency Integrated Circuits Cambridge University Press

With the exponential growth of the number of Internet nodes, the volume of the data transported on the backbone has increased with the same trend. The load of the global Internet backbone will soon increase to tens of terabits per second. This indicates that the backbone bandwidth requirements will increase by a factor of 50 to 100 every seven years. Transportation of such high volumes of data requires suitable media with low loss and high bandwidth. Among the available transmission media, optical fibers achieve the best performance in terms of loss and bandwidth. High-speed data can be transported over hundreds of kilometers of single-mode fiber without significant loss in signal integrity. These fibers progressively benefit from reduction of cost and improvement of performance. Meanwhile, the electronic interfaces used in an optical network are not capable of exploiting the ultimate bandwidth of the fiber, limiting the throughput of the network. Different solutions at both the system and the circuit levels have been proposed to increase the data rate of the backbone. System-level solutions are based on the utilization of wave-division multiplexing (WDM), using different colors of light to transmit several sequences simultaneously. In parallel with that, a great deal of effort has been put into increasing the operating rate of the electronic transceivers using highly-developed fabrication processes and novel circuit techniques.

The RF and Microwave Handbook John Wiley & Sons

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery circuits, and multiplexers. This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

Beyond 100 GHz Cambridge University Press

This advanced text and reference covers the design and implementation of integrated circuits for analog-to-digital and digital-to-analog conversion. It begins with basic concepts and systematically leads the reader to advanced topics, describing design issues and techniques at both circuit and system level. Gain a system-level perspective of data conversion units and their trade-offs with this state-of-the-art book. Topics covered include: sampling circuits and architectures, D/A and A/D architectures; comparator and op amp design; calibration techniques; testing and characterization; and more!