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Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs,  $x$  and  $y$ ; and one output  $z$  is

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Q. 1.1: List the octal and hexadecimal numbers from 16 to 32. Using A and B for the last two digits Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that the AND or OR **What Is Digital Design? - Deepend**

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**a full-subtractor circuit incremen** Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the Lecture 4: Digital Logic Gates شرح بالعربي تصميم المنطق دوائر المنطق Revised worksheet Lecture 9: NAND and NOR (Universal Gates) Implementation

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