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This paper provides the implementation of the transmitterAutomated performance-based design technique for an ...In order to support high-definition video transmission, an implementation of video transmission system based on Long Term Evolution is designed. This system is developed on Xilinx Virtex-6 FPGA...FPGA Implementation of Video Transmission System Based on LTEImplementation of an efficient turbo decoder with low complexity, short delay and insignificant performance degradation is currently a quite challenging task. The paper presents an implementation of a 3GPP TS 36.212 LTE turbo decoder. The design of the turbo decoder has been optimized to achieve efficient FPGA resource utilization.FPGA implementation of LTE turbo decoder using MAX-log MAP ...FPGA Implementation of Turbo Decoder for LTE Standard . By S. Rajaram, A. Sakthi Amutha Vardhini and K. Kalyani. Abstract. The data rate of 100 Mbps will be supported by upcoming 3G Long Term Evolution (LTE) standard. In 20 MHz of bandwidth, this data rate will be attained. For the arrival of high data rate of the 3G LTE systems, there is an ...FPGA Implementation of Turbo Decoder for LTE Standard - COREAbstract This paper describes the implementation on field programmable gate array (FPGA) of a turbo decoder for 3GPP Long-Term Evolution standard. Considering the high data rates required by this standard, parallel decoding architecture is used.

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