

Comparing Topologies And The Design Rules Of The Game

Methodologies and Application Issues of Contemporary Computing Framework
 Designing Network On-Chip Architectures in the Nanoscale Era
 Permanent Magnet Synchronous Machines
 Integrated Circuit and System Design. Power and Timing Modeling, Optimization, and Simulation
 Advanced Circuits for Emerging Technologies
 Multidisciplinary Design Optimization Methods for Electrical Machines and Drive Systems
 From High Speed to Low Energy
 Advances in Electrical and Computer Technologies
 Bridging the Gap Between Theory and Practice
 Proceedings of the 1995 International Conference on Parallel Processing
 Topology Optimization in Structural and Continuum Mechanics
 Subthreshold Source-Coupled Circuits
 CML, ECL and SCL Digital Circuits
 Proceedings of the 2017 International Conference on Mechanical Design (ICMD2017)
 Magnetic Materials and Soft-switched Topologies for High-current DC-DC Converters
 11th International IFIP-TC6 Conference, ONDM 2007, Athens, Greece, May 29-31, 2007, Proceedings
 Real-World Applications of Genetic Algorithms
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 Comparison of Single Stage and Two Stage Grid-tie Inverters
 Pushing the Limits of Fully Integrated Power Management
 Second International Symposium, ISPA 2004, Hong Kong, China, December 13-15, 2004, Proceedings
 Computational Systems Bioinformatics
 Extreme Low-Power Mixed Signal IC Design
 Circuits for Emerging Applications
 Multiphysics Simulation by Design for Electrical Machines, Power Electronics and Drives
 Soft Magnetic Composites in Novel Designs of Electrical Traction Machines
 Device Characterization, Topology Evaluation, and Design
 Flip-Flop Design in Nanometer CMOS
 VLSI
 Model and Design of Improved Current Mode Logic Gates
 Power and Timing Modeling, Optimization and Simulation; 14th International Workshop, PATMOS 2004, Santorini, Greece, September 15-17, 2004, Proceedings
 Parallel and Distributed Processing and Applications
 Integrated Circuit and System Design
 Design, Measurement and Management of Large-Scale IP Networks
 Mixed-Signal Circuits
 Radiation Tolerant Electronics

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BAKER SIENA

Methodologies and Application Issues of Contemporary Computing Framework Springer Nature
 The book will address the-state-of-the-art in integrated circuit design in the context of emerging systems. New exciting opportunities in body area networks, wireless communications, data networking, and optical imaging are discussed. Emerging materials that can take system performance beyond standard CMOS, like Silicon on Insulator (SOI), Silicon Germanium (SiGe), and Indium Phosphide (InP) are explored. Three-dimensional (3-D) CMOS integration and co-integration with sensor technology are described as well. The book is a must for anyone serious about circuit design for future technologies. The book is written by top notch international experts in industry and academia. The intended audience is practicing engineers with integrated circuit background. The book will be also used as a recommended reading and supplementary material in graduate course curriculum. Intended audience is professionals working in the integrated circuit design field. Their job titles might be : design engineer, product manager, marketing manager, design team leader, etc. The book will be also used by graduate students. Many of the chapter authors are University Professors.
Designing Network On-Chip Architectures in the Nanoscale Era Springer
 Interest in permanent magnet synchronous machines (PMSMs) is continuously increasing worldwide, especially with the increased use of renewable energy and the electrification of transports. This book contains the successful submissions of fifteen papers to a Special Issue of Energies on the subject area of "Permanent Magnet Synchronous Machines". The focus is on permanent magnet synchronous machines and the electrical systems

they are connected to. The presented work represents a wide range of areas. Studies of control systems, both for permanent magnet synchronous machines and for brushless DC motors, are presented and experimentally verified. Design studies of generators for wind power, wave power and hydro power are presented. Finite element method simulations and analytical design methods are used. The presented studies represent several of the different research fields on permanent magnet machines and electric drives.

Permanent Magnet Synchronous Machines Springer Science & Business Media

Proceedings of the NATO Advanced Research Workshop, Sesimbra, Portugal, June 20-26, 1992

Integrated Circuit and System Design. Power and Timing Modeling, Optimization, and Simulation World Scientific

Design exibility and power consumption in addition to the cost, have always been the most important issues in design of integrated circuits (ICs), and are the main concerns of this research, as well. Energy Consumptions: Power dissipation (P) and energy consumption are - diss pecially importantwhen there is a limited amountof power budgetor limited source of energy. Very common examples are portable systems where the battery life time depends on system power consumption. Many different techniques have been - veloped to reduce or manage the circuit power consumption in this type of systems. Ultra-low power (ULP) applications are another examples where power dissipation is the primary design issue. In such applications, the power budget is so restricted that very special circuit and system level design techniquesare needed to satisfy the requirements. Circuits employed in applications such as wireless sensor networks (WSN), wearable battery powered systems [1], and implantable circuits for biological applications need to consume very low amount of power such that the entire system can survive for a very long time without the need for changingor recharging battery[2-4]. Using newpowersupplytechniquessuchas energyharvesting[5]and printable batteries [6], is another reason for

reducing power dissipation. Developing special design techniques for implementing low power circuits [7–9], as well as dynamic power management (DPM) schemes [10] are the two main approaches to control the system power consumption. Design Flexibility: Design exibility is the other important issue in modern integrated systems.

Advanced Circuits for Emerging Technologies Analysis and Design of Power Converter Topologies for Application in Future More Electric Aircraft This volume contains about 40 papers covering many of the latest developments in the fast-growing field of bioinformatics. The contributions span a wide range of topics, including computational genomics and genetics, protein function and computational proteomics, the transcriptome, structural bioinformatics, microarray data analysis, motif identification, biological pathways and systems, and biomedical applications. There are also abstracts from the keynote addresses and invited talks. The papers cover not only theoretical aspects of bioinformatics but also delve into the application of new methods, with input from computation, engineering and biology disciplines. This multidisciplinary approach to bioinformatics gives these proceedings a unique viewpoint of the field. Contents: Exploring the Ocean's Microbes: Sequencing the Seven Seas (M E Frazier et al.) Protein Network Comparative Genomics (T Ideker) Bioinformatics at Microsoft Research (S Mercer) Protein Fold Recognition Using Gradient Boost Algorithm (F Jiao et al.) Efficient Annotation of Non-Coding RNA Structures Including Pseudoknots via Automated Filters (C Liu et al.) Efficient Generalized Matrix Approximations for Biomarker Discovery and Visualization in Gene Expression Data (W Li et al.) Sorting Genomes by Translocations and Deletions (X Qi et al.) Detection of Cleavage Sites for HIV-1 Protease in Native Proteins (L You) Identifying Biological Pathways via Phase Decomposition and Profile Extraction (Y Zhang & Z Deng) Complexity and Scoring Function of MS/MS Peptide De Novo Sequencing (C Xu & B Ma) Simulating In Vitro Epithelial Morphogenesis in Multiple Environments (M R Grant et al.) and other papers Readership: Research and application community in bioinformatics, systems biology, medicine, pharmacology and biotechnology. A useful reference for graduate researchers in bioinformatics and computational biology. Keywords: Bioinformatics; Computational Biology; Genomics; Proteomics; Structural Biology; Biological Pathways; Phylogenetics; Systems Biology Key Features: The CSB meetings accepts only the highest quality research paper, with a paper-acceptance rate of below 20% The CSB meeting represents a unique bioinformatics conference in which papers blend bioinformatic tool development with in silico biology CSB meetings have become one of the most well attended bioinformatics conferences CSB proceedings are indexed by Medline *Multidisciplinary Design Optimization Methods for Electrical Machines and Drive Systems* Springer Science & Business Media APEC focuses on the practical and applied aspects of the power electronics business The conference addresses issues of immediate and long term importance to practicing power electronics engineer

From High Speed to Low Energy IET

This book presents MOSFET-based current mode logic (CML) topologies, which increase the speed, and lower the transistor count, supply voltage and power consumption. The improved topologies modify the conventional PDN, load, and the current source sections of the basic CML gates. Electronic system implementation involves embedding digital and analog circuits on a single die shifting towards mixed-mode circuit design. The high-resolution, low-power and low-voltage analog circuits are combined with high-frequency complex digital circuits, and the conventional static CMOS logic generates large current spikes during the switching (also referred to as digital switching noise), which degrade the resolution of the sensitive analog circuits via supply line and substrate coupling. This problem is exacerbated further with scaling down of CMOS technology due to higher integration levels and operating frequencies. In the literature, several methods are described to reduce the propagation of the digital switching noise. However, in high-resolution applications, these methods are not sufficient. The conventional CMOS static logic is no longer an effective solution, and therefore an alternative with reduced current spikes or that draws a constant supply current must be selected. The current mode logic (CML) topology, with its unique property of requiring constant supply current, is a promising alternative to the conventional CMOS static logic.

Advances in Electrical and Computer Technologies Springer

Research on radiation-tolerant electronics has increased rapidly over the past few years, resulting in many interesting approaches to modeling radiation effects and designing radiation-hardened integrated circuits and embedded systems. This research is strongly driven by the growing need for radiation-hardened electronics for space applications, high-energy physics experiments such as those on the Large Hadron Collider at CERN, and many terrestrial nuclear applications including nuclear energy and nuclear safety. With the progressive scaling of integrated circuit technologies and the growing complexity of electronic systems, their susceptibility to ionizing radiation has raised many exciting challenges, which are expected to drive research in the coming decade. In this book we highlight recent breakthroughs in the study of radiation effects in advanced semiconductor devices, as well as in high-performance analog, mixed signal, RF, and digital integrated circuits. We also focus on advances in embedded radiation hardening in both FPGA and microcontroller systems and apply radiation-hardened embedded systems for cryptography and image processing, targeting space applications.

Bridging the Gap Between Theory and Practice CRC Press

Recently the world celebrated the 60th anniversary of the invention of the first transistor. The first integrated circuit (IC) was built a decade later, with the first microprocessor designed in the early 1970s. Today, ICs are a part of nearly every aspect of our daily lives. They help us live longer and more comfortably, and do more, faster. All this is possible because of the relentless search for new materials, circuit designs, and ideas happening on a daily basis at industrial and academic institutions around the globe. Showcasing the latest advances in very-large-scale integrated (VLSI) circuits, VLSI: Circuits for Emerging Applications provides a balanced view of industrial and academic developments beyond silicon and complementary metal-oxide-semiconductor (CMOS) technology. From quantum-dot cellular automata (QCA) to chips for cochlear implants, this must-have resource: Investigates the trend of combining multiple cores in a single chip to boost performance of the overall system Describes a novel approach to enable physically unclonable functions (PUFs) using intrinsic features of a VLSI chip Examines the VLSI implementations of major symmetric and asymmetric key cryptographic algorithms, hash functions, and digital signatures Discusses nonvolatile memories such as resistive random-access memory (ReRAM), magneto-resistive RAM (MRAM), and floating-body RAM (FB-RAM) Explores organic transistors, soft errors, photonics, nanoelectromechanical (NEM) relays, reversible computation, bioinformatics, asynchronous logic, and more VLSI: Circuits for Emerging Applications presents cutting-edge research, design architectures, materials, and uses for VLSI circuits, offering valuable insight into the current state of the art of micro- and

nanoelectronics.

Proceedings of the 1995 International Conference on Parallel Processing KIT Scientific Publishing

This set of technical books contains all the information presented at the 1995 International Conference on Parallel Processing. This conference, held August 14 - 18, featured over 100 lectures from more than 300 contributors, and included three panel sessions and three keynote addresses. The international authorship includes experts from around the globe, from Texas to Tokyo, from Leiden to London. Compiled by faculty at the University of Illinois and sponsored by Penn State University, these Proceedings are a comprehensive look at all that's new in the field of parallel processing.

Topology Optimization in Structural and Continuum Mechanics kassel university press GmbH

This book constitutes the refereed proceedings of the 14th International Workshop on Power and Timing Optimization and Simulation, PATMOS 2004, held in Santorini, Greece in September 2004. The 85 revised papers presented together with abstracts of 6 invited presentations were carefully reviewed and selected from 152 papers submitted. The papers are organized in topical sections on buses and communication, circuits and devices, low power issues, architectures, asynchronous circuits, systems design, interconnect and physical design, security and safety, low-power processing, digital design, and modeling and simulation.

Subthreshold Source-Coupled Circuits CRC Press

This book gives a detailed analysis of switched-capacitor DC-DC converters that are entirely integrated on a single chip and establishes that these converters are mainly limited by the large parasitic coupling, the low capacitor energy density, and the fact that switched-capacitor converter topologies only have a fixed voltage conversion ratio. The authors introduce the concept of Advanced Multiphasing as a way to circumvent these limitations by having multiple out-of-phase parallel converter cores interact with each other to minimize capacitor charging losses, leading to several techniques that demonstrate record efficiency and power-density, and even a fundamentally new type of switched-capacitor topology that has a continuously-scalable conversion ratio. Provides single-source reference to the recently-developed Advanced Multiphasing concept; Enables greatly improved performance and capabilities in fully integrated switched-capacitor converters; Enables readers to design DC-DC converters, where multiple converter cores are put in parallel and actively interact with each other over several phases to improve their capabilities.

CML, ECL and SCL Digital Circuits John Wiley & Sons

Going beyond isolated research ideas and design experiences, Designing Network On-Chip Architectures in the Nanoscale Era covers the foundations and design methods of network on-chip (NoC) technology. The contributors draw on their own lessons learned to provide strong practical guidance on various design issues. Exploring the design process of the network, the first part of the book focuses on basic aspects of switch architecture and design, topology selection, and routing implementation. In the second part, contributors discuss their experiences in the industry, offering a roadmap to recent products. They describe Tiler's TILE family of multicore processors, novel Intel products and research prototypes, and the TRIPS operand network (OPN). The last part reveals state-of-the-art solutions to hardware-related issues and explains how to efficiently implement the programming model at the network interface. In the appendix, the microarchitectural details of two switch architectures targeting multiprocessor system-on-chips (MPSoCs) and chip multiprocessors (CMPs) can be used as an experimental platform for running tests. A stepping stone to the evolution of future chip architectures, this volume provides a how-to guide for designers of current NoCs as well as designers involved with 2015 computing platforms. It cohesively brings together fundamental design issues, alternative design paradigms and techniques, and the main design tradeoffs—consistently focusing on topics most pertinent to real-world NoC designers.

Proceedings of the 2017 International Conference on Mechanical Design (ICMD2017) Springer

This book constitutes the refereed proceedings of the 11th International IFIP-TC6 Conference on Optical Network Design and Modeling, ONDM 2007, held in Athens, Greece, in May 2007. The 41 revised full papers presented together with 14 invited papers address all recent advances in the design, modeling and implementation of optical networks.

Magnetic Materials and Soft-switched Topologies for High-current DC-DC Converters Springer Nature

The book addresses some of the most recent issues, with the theoretical and methodological aspects, of evolutionary multi-objective optimization problems and the various design challenges using different hybrid intelligent approaches. Multi-objective optimization has been available for about two decades, and its application in real-world problems is continuously increasing. Furthermore, many applications function more effectively using a hybrid systems approach. The book presents hybrid techniques based on Artificial Neural Network, Fuzzy Sets, Automata Theory, other metaheuristic or classical algorithms, etc. The book examines various examples of algorithms in different real-world application domains as graph growing problem, speech synthesis, traveling salesman problem, scheduling problems, antenna design, genes design, modeling of chemical and biochemical processes etc.

11th International IFIP-TC6 Conference, ONDM 2007, Athens, Greece, May 29-31, 2007, Proceedings John Wiley & Sons

High frequency power conversion is attractive for the opportunities it affords for improved performance. Dc-dc converters operating at high frequencies use smaller-valued energy storage elements, which tend to be physically smaller and lower-cost, and this can result in improved transient performance while retaining high efficiency. One way to achieve high switching frequencies is by using resonant inverter and rectifier topologies and regulating voltage via on-off control. This scheme requires a great deal of investigation of design practices appropriate to high frequency power conversion. The design issues were investigated for a 200 W 160-200 V input 33 V output converter. A comparison of resonant inverter topologies for the power stage was made. Appropriate devices were sought, compared, and characterized. A high frequency gate drive scheme for a large vertical MOSFET was developed. Several prototypes were built and these are also presented.

Real-World Applications of Genetic Algorithms Springer

This book brings together the diversified areas of contemporary computing frameworks in the field of Computer Science, Engineering and Electronic Science. It focuses on various techniques and applications pertaining to cloud overhead, cloud infrastructure, high speed VLSI circuits, virtual machines, wireless and sensor networks, clustering and extraction of information from images and analysis of e-mail texts. The state-of-the-art methodologies and techniques are addressed in chapters presenting various proposals for enhanced outcomes and performances. The techniques

discussed are useful for young researchers, budding engineers and industry professionals for applications in their respective fields.

Topology Optimization Design of Heterogeneous Materials and Structures CRC Press

This thesis compares two methods of designing grid-tie inverters. The first design topology is a traditional two stage approach consisting of an isolated DC-DC converter on the input followed by a high switching frequency SPWM (Sinusoidal Pulse Width Modulation) stage to produce the required low frequency sine wave output. The novel second design approach employs a similar DC-DC input stage capable of being modulated to provide a rectified sine wave output voltage/current waveform. This stage is followed by a simple low frequency switched Unfolding Stage to recreate the required sine wave output. Both of the above designs have advantages and disadvantages depending on operating parameters. The following work will compare the Unfolding Output Stage and the SPWM Output Stage at various power levels and power densities. Input stage topologies are similarly examined in order to determine the best design approach for each output stage under consideration.

Model and Design of Bipolar and MOS Current-Mode Logic Springer Nature

This book constitutes the refereed proceedings of the 20th International Conference on Integrated Circuit and System Design, PATMOS 2010, held in Grenoble, France, in September 2010. The 24 revised full papers presented and the 9 extended abstracts were carefully reviewed and are organized in topical sections on design flows; circuit techniques; low power circuits; self-timed circuits; process variation; high-level modeling of poweraware heterogeneous designs in SystemC-AMS; and minalogic.

Analysis and Design of Power Converter Topologies for Application in Future More Electric Aircraft MDPI

This book presents the research challenges that are due to the introduction of the 3rd dimension in chips for researchers and covers the whole architectural design approach for 3D-SoCs. Nowadays the 3D-Integration technologies, 3D-Design techniques, and 3D-Architectures are emerging as interesting, truly hot, broad topics. The present book gathers the recent advances in the whole domain by renowned experts in the field to build a comprehensive and consistent book around the hot topics of three-dimensional architectures and micro-architectures. This book includes contributions from high level international teams working in this field.